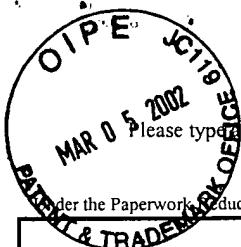


2815\$



Please type plus sign (+) inside this box → [+]

PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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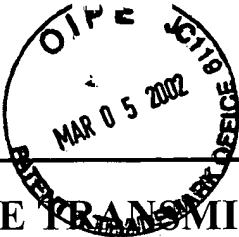
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application Number	08/520,079
		Filing Date	August 28, 1995
		First Named Inventor	Shunpei YAMAZAKI et al.
		Group Art Unit	2815
		Examiner Name	P. E. Brock II
Total Number of Pages in This Submission		Attorney Docket Number	740756-1400

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input checked="" type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input checked="" type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Declaration and Power of Attorney <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Other <div style="transform: rotate(-90deg); position: absolute; right: 0; bottom: 0;"> RECEIVED MAR 12 2002 TECHNOLOGY CENTER 2800 </div>
Remarks	<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 19-2380 for the above identified docket number.	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Luan C. Do, Reg. No. 38,434 Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22102
Signature	
Date	February 25, 2002

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 2/25/02	
Type or printed name	BONNIE GARILLI
Signature	
Date	February 25, 2002

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.



FEE TRANSMITTAL FOR FY 2002

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$688.00)

Complete if Known

Application Number 08/520,079
Filing Date August 28, 1995
First Named Inventor Shunpei YAMAZAKI et al.
Examiner Name P. E. Brock II
Group Art Unit 2815
Attorney Docket No. 740756-1400

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METHOD OF PAYMENT

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit Account Number 19-2380

Deposit Account Name Nixon Peabody LLP

- ☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

☐ Applicant claims small entity status. See 37 CFR 1.27

2. ☒ Payment Enclosed:

☒ Check ☐ Credit Card ☐ Money Order ☐ Other

FEE CALCULATION

1. BASIC FILING FEE

Large Entity Fee Code	Large Entity Fee (\$)	Small Entity Fee Code	Small Entity Fee (\$)	Fee Description	Fee Paid
101	740	201	370	Utility filing fee	
106	330	206	165	Design filing fee	
107	510	207	255	Plant filing fee	
108	740	208	370	Reissue filing fee	
114	160	214	80	Provisional filing fee	

SUBTOTAL (1) (\$)

2. EXTRA CLAIM FEES

Extra Claims Fee from below Fee Paid
Total Claims 78 -72** = 6 X 18 = \$108.00
Independent Claims 9 -10** = 0 X 0 = 0
Multiple Dependent 0 = 0

Large Entity Fee Code	Large Entity Fee (\$)	Small Entity Fee Code	Small Entity Fee (\$)	Fee Description
103	18	203	9	Claims in excess of 20
102	84	202	42	Independent claims in excess of 3
104	280	204	140	Multiple dependent claim, if not paid
109	84	209	42	** Reissue independent claims over original patent
110	18	210	9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$108.00)

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Fee Code	Large Entity Fee (\$)	Small Entity Fee Code	Small Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English transaction	
147	2,520	147	2,520	For filing a request for <i>ex parte</i> reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	216	200	Extension for reply within second month	400.00
117	920	217	460	Extension for reply within third month	
118	1,440	218	720	Extension for reply within fourth month	
128	1,960	228	980	Extension for reply within fifth month	
119	320	219	160	Notice of Appeal	
120	320	220	160	Filing a brief in support of an appeal	
121	280	221	140	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,280	241	640	Petition to revive - unintentional	
142	1,280	242	640	Utility issue fee (or reissue)	
143	460	243	230	Design issue fee	
144	620	244	310	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Processing fee under 37 CR 1.17(q)	
126	180	126	180	Submission of Information Disclosure Stmt	180.00
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	740	246	370	Filing a submission after final rejection (37 CFR § 1.129(a))	
149	740	249	370	For each additional invention to be examined (37 CFR § 1.29(b))	
179	740	279	370	Request for Continued Examination (RCE)	
169	900	169	900	Request for expedited examination of a design application	

Other fee (specify) _____

* Reduced by Basic Filing Fee Paid SUBTOTAL (3) (\$580.00)

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[Signature]

SUBMITTED BY

Name (Print/Type)	Luan C. Do	Registration No. (Attorney/Agent)	38,434	Telephone	703-790-9110
Signature	<i>[Signature]</i>			Date	February 25, 2002

Complete (if applicable)



Docket No. 740756-1400
Serial No. 08/520,079

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/520,079) Group Art Unit: 2815
Filed: August 28, 1995) Examiner: P. Brock II
For: SEMICONDUCTOR CIRCUIT FOR)
ELECTRO-OPTICAL DEVICE AND)
METHOD OF MANUFACTURING)
THE SAME) Monday, February 25, 2002

#50/Amended
Amended
4/4/02
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TECHNOLOGY CENTER 2800

CERTIFICATE OF MAILING

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on Feb 25 2002

Donna A. Davis

The Office Action of September 25, 2001 was received and carefully reviewed. Reconsideration and withdrawal of the currently pending rejections are requested for the reasons advanced in detail below.

IN THE CLAIMS:

Please cancel claims 117, 118, 120, 121 and 122.

Please amend claims 73, 80, 87, 93, 99, 105, 111, 123 and 129 to read as follows:

- 51
73. (Thrice Amended) A thin film transistor comprising:
a crystalline semiconductor island over a substrate having an insulating surface;
source and drain regions in said semiconductor island;
a channel forming region between said source and drain regions;
a gate insulating film adjacent to at least said channel forming region;